

## **HP0-S16 Practice Test**

### **HP Implementing HP BladeSystem**

#### **HP0-S16: Implementing HP BladeSystem**

**Practice Exam:** HP0-S16 Exams

**Exam Number/Code:** HP0-S16

**Exam Name:** Implementing HP BladeSystem

**Questions and Answers:** 59 Q&As

**( Certification III )**



Exam : [HP0-S16](#)

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This HP0-S16 PDF demo do not include the questions and answers's picture:

Exam : HP HP0-S16

Title : Implementing HP BladeSystem

1. Which storage technology is used with Serial Attached SCSI?

- A. NUMA
- B. shared bus
- C. one-to-many
- D. point-to-point

Answer: D

2. Which options exist when you enable HP NIC teaming? (Select two.)

- A. fault tolerance
- B. CISCO Layer 4 teaming
- C. socket layered balancing
- D. NIC assisted load balancing
- E. switch assisted load balancing

Answer: AE

3. Which HP BladeSystem c-Class server blades support HP-UX? (Select two.)

- A. BL465c
- B. BL680c
- C. BL685c
- D. BL860c
- E. BL870c
- F. BL2x220c

Answer: DE

4. What is the standard HP warranty on optional components such as mezzanines, fans, and power supplies?

- A. 1-1-1 (1 year parts; 1 year labor; 1 year onsite limited global warranty)
- B. 3-3-3 (3 year parts; 3 year labor; 3 year onsite limited global warranty)
- C. 1-0-0 (1 year parts limited global warranty or the system warranty, whichever is longer)
- D. 1-1-0 (1 year parts limited global warranty or the system warranty, whichever is longer; 1 year labor)

Answer: C

5. How is BladeSystem Integrated Manager installed by default?

- A. as part of a SmartStart installation
- B. with the initial installation of HP SIM
- C. as an add-on after HP SIM is installed
- D. with a separate license key in HP SIM

Answer: B

6. What is the minimum number of power supplies required in a BladeSystem c-Class when using three-phase power?

- A. 2
- B. 3
- C. 6
- D. 8

Answer: C

7. What is the function of ECC memory?

- A. It only detects single-bit errors.
- B. It detects and corrects single-bit errors.
- C. It detects multi-bit errors within a single DRAM chip.
- D. It detects and corrects multi-bit errors within a single DRAM chip.

Answer: B

8. Which Onboard Administrator is the primary by default?

- A. the left one
- B. the right one
- C. the active one
- D. the first one installed

Answer: A

9. Which processors use a 64-bit x86-based architecture? (Select two.)

- A. Intel Itanium
- B. AMD Opteron
- C. Intel Pentium IV
- D. Intel Pentium M
- E. Intel Xeon Quad Core

Answer: BE

10. Which HP server blade uses the Itanium processor?

- A. SB40c
- B. BL485c
- C. BL685c
- D. BL860c
- E. BL2x220c

Answer: D

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